

## **Features**

- Lead free as standard
- RoHS compliant\*
- Low forward voltage
- Extremely thin/leadless package
- Majority carrier conduction
- Designed for mounting on small surface

## **Applications**

- Cellular phones
- PDAs
- Notebooks
- GPS

# CD1005-B0520 - Surface Mount Schottky Barrier Diode

#### **General Information**

The markets of portable communications, computing and video equipment are challenging the semiconductor industry to develop increasingly smaller electronic components. Bourns offers Schottky Barrier Diodes for these applications, in compact chip package 1005 (SOD323) size format, which offer PCB real estate savings and are considerably smaller than most competitive parts. The Schottky Barrier Diodes offer a forward current of 0.5 A with a repetitive peak reverse voltage of 30 V.

Bourns® Chip Diodes conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.

#### Electrical Characteristics (@ TA = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Min.	Nom.	Max.	Unit
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>			30	V
Reverse Voltage	VR			20	V
Average Forward Rectified Current	lo			0.5	А
Maximum Instantaneous Forward Voltage @ IF = 1.0 A	VF		0.45	0.55	V
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	Ігэм			2	А
Forward Voltage (I <sub>F</sub> = 100 mA)	VF		0.25	0.36	V
Forward Voltage (I <sub>F</sub> = 500 mA)	VF		0.35	0.47	V
Reverse Current (V <sub>R</sub> = 20 V)	l <sub>R</sub>			100	μΑ
Capacitance between terminals (f = 1 MHz, and 0 VDC reverse voltage)	Ст		100		pF

#### Thermal Characteristics (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

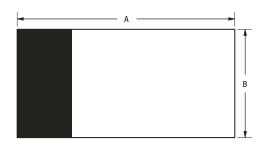
Parameter	Symbol	CD1005-B0520	Unit
Junction Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	Тѕтс	-40 to +125	°C

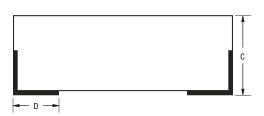
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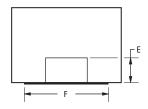
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#### **Product Dimensions**

This is a lead free product. It is a SOD-323F (1005) standard package, molded plastic. The terminals are Gold plated and are solderable per MIL-STD-750, Method 2026. The polarity is indicated by a cathode band. The package weighs approximately 0.006 g. The package and dimensions are shown below

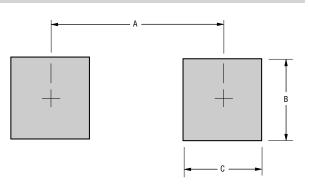






Dimensions		
А	2.40 - 2.60	
	(0.095 - 0.102)	
В	1.10 - 1.30	
	(0.043 - 0.051)	
С	0.70 - 0.90	
	(0.027 - 0.035)	
D	0.55 Typ	
	(0.021) Typ.	
E	$\frac{0.30}{(0.012)}$ Typ.	
	(0.012) Typ.	
F	$\frac{1.0}{(0.20)}$ Typ.	
<u>'</u>	(0.39) Typ.	

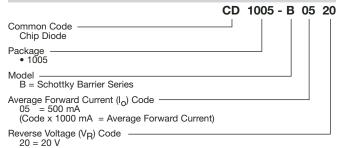
#### **Recommended Pad Layout**



Dimension	1005
A (Max.)	2.10
	(0.082)
B (Min.)	1.20
	(0.047)
C (Min.)	1.20
	(0.047)

DIMENSIONS:  $\frac{MM}{(INCHES)}$ 

## **How To Order**

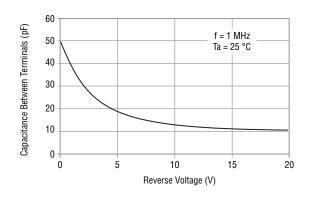


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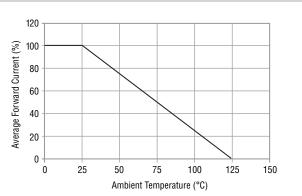
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### **Rating and Characteristic Curves**

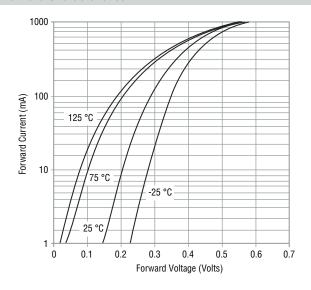
#### **Typical Capacitance**



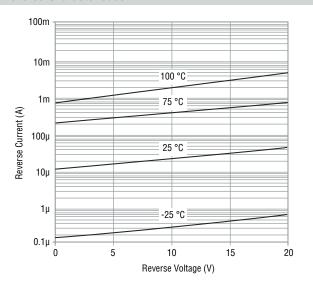
## **Current Derating Curve**



#### **Forward Characteristics**

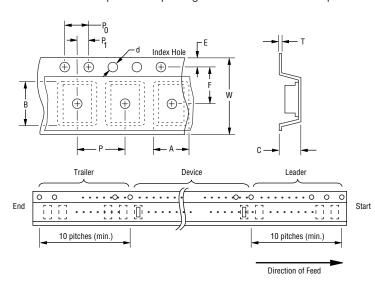


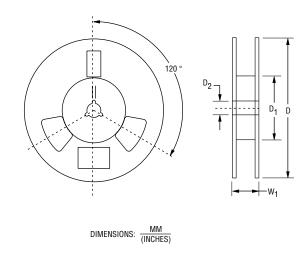
#### **Reverse Characteristics**



#### **Packaging Information**

The surface mount product is packaged in an 8 mm x 4 mm tape and reel format per EIA-481 standard.





Item	Symbol	1005 (SOD323)
Carrier Width	А	1.90 ±0.10
Carrier Length	В	(0.075 - 0.004) 4.30 ±0.10
Carrier Length		(0.169 - 0.004)
Carrier Depth	С	1.80 ±0.10 (0.071 - 0.004)
Sprocket Hole	d	1.55 ±0.05 (0.061 - 0.002)
Reel Outside Diameter	D	178 (7.008)
Reel Inner Diameter	D <sub>1</sub>	80.0 (3.150) Min.
Feed Hole Diameter	D <sub>2</sub>	13.0 ±0.20 (0.512 - 0.008)
Sprocket Hole Position	E	1.75 ±0.10 (0.069 - 0.004)
Punch Hole Position	F	$\frac{3.50 \pm 0.05}{(0.138 - 0.002)}$
Punch Hole Pitch	Р	4.00 ±0.10 (0.157 - 0.004)
Sprocket Hole Pitch	P <sub>0</sub>	4.00 ±0.10 (0.157 - 0.004)
Embossment Center	P <sub>1</sub>	2.00 ±0.05 (0.079 - 0.002)
Overall Tape Thickness	Т	0.20 ±0.10 (0.008 - 0.004)
Tape Width	W	8.00 ±0.20 (0.315 - 0.008)
Reel Width	W <sub>1</sub>	$\frac{13.5}{(0.531)}$ Max.
Quantity per Reel		2,500



## Reliable Electronic Solutions

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\*RoHS Directive 2002/95/EC Jan 27 2003 including Annex Specifications are subject to change without notice. Customers should verify actual device performance in their specific applications.